Abstract
The purpose of this project is to research and develop algorithms, architectures, and applications for network processing/processors and embedded systems. This project is a continuation of the PITA project funded in the last year. Specific goals of this year’s continuing work include: (1) designing methodologies, algorithms, and software tools for network processing and other embedded systems; (2) developing methodologies and tools for debugging distributed and embedded systems; and (3) developing curriculum and educating students in these areas. These goals will be accomplished through collaborative efforts between Lehigh University, Agere Systems (formerly Lucent), and Carnegie Mellon University, with support from PITA.